

35711 U.S. PTO

10/045376

11/09/01

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

| APPL NUM | FILING DATE | CLASS | SUBCLASS | GAU | EXAMINER |
|----------|-------------|-------|----------|------|----------|
| 10045376 | 11/09/2001 | 428 | 297 | 2812 | |

**APPLICANTS: Doyle Brian; Roberts Brian;

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A DIV OF 09/342,030 06/28/1999 PAT 6,362,082

** FOREIGN APPLICATIONS VERIFIED: N/A

| | | |
|---|---|--|
| PG-PUB | DO NOT PUBLISH <input type="checkbox"/> | RESCIND <input type="checkbox"/> |
| Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no Verified and Acknowledged Examiners's initials <i>JK</i> | | ATTORNEY DOCKET NO 42390 P5768D <i>see New #76</i> |
| TITLE : Methodology for control of short-channel effects in MOS transistors <small>U.S. DEPT. OF COM. / PAT. & TM. PTO-151 (Rev. 12-93)</small> | | |

| NOTICE OF ALLOWANCE MAILED | | CLAIMS ALLOWED | |
|---|-----------|--|-----------------|
| | | Total Claims | Print Claim for |
| | | 16 | 0.6 1 |
| ISSUE FEE | | DRAWING | |
| Amount Due | Date Paid | Sheets Drawn | Print Fig. |
| | | 7 | 18 18 |
| | | Applicant Examiner | |
| <input type="checkbox"/> TERMINAL DISCLAIMER | | PREPARED FOR ISSUE WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only. | |

FILED WITH:

☐ DISK (CRF)☐ CD-ROM

(Attached in pocket on right inside flap)